ARRAY-TYPE STORAGE DEVICE

Patent number:

JP9288547

Publication date:

1997-11-04

Inventor:

YOSHIDA TORU; AOKI TAKESHI; TATESHIMO

MASASHI

Applicant:

HITACHI INF TECHNOL:KK

Classification:

- international:

G06F3/06; G06F3/06

- european:

Application number: JP19960124088 19960422

Priority number(s):

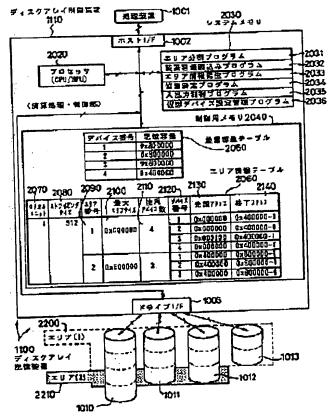
Report a data error here

Abstract of JP9288547

PROBLEM TO BE SOLVED: To eliminate the restriction of the storage capacity of storage devices to be mounted by providing a specified area setting means, an address calculation

means and the like.

SOLUTION: A disk array controller 1110 and disk storage devices 1010-1013 are provided. In the device, the area setting means sets a first storage area for allocating a first address space by adjusting it to a first largest storage capacity which is the smallest in the storage capacity of the storage devices 1010-1013 and forming an array, allocates a second address space by adjusting it to a second largest storage capacity which is the smallest among remaining capacities obtained by subtracting first largest storage capacity from respective largest storage capacities in plural storage devices except for the storage device of the first largest storage capacity which is the smallest and forms the array. An address calculation means calculates the physical addresses of the storage devices 1010-1013 in accordance with the first or second storage area forming the array based on an access address from a host processor.



Data supplied from the esp@cenet database - Patent Abstracts of Japan